INFORMATION DISCLOSURE CITATION *(Use several sheets if necessary)*	Docket Number (Optional) IDTL002US1-1826		Application Number TBA
	Applicant(s) Talledo, Cesar A. et al.		
	Filing Date July 15, 2003	Group Art Unit	TBA

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	NO

OTHER *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		Ryan, Kevin, "DDR SDRAM Functionality and Controller Read Data Capture," DesignLine, Micron Technology, Inc., Volume 8, Issue 3, 3Q99.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.